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1. Design and implementation of Adder and Subtractor using logic gates. 2. Design and implementation of code converters using logic gates (i) BCD to excess-3 code and vice versa (ii) Binary to gray and vice-versa3. Design and implementation of 4 bit binary Adder Subtractor and BCD adder using IC 74834. Design and implementation of 2 bit Magnitude Comparator using logic gates 8 Bit Magnitude Compa...

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